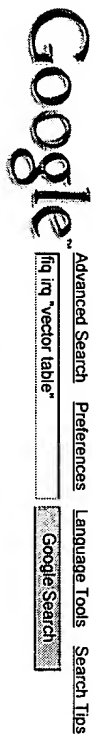


<http://www.google.com/search?q=fig+irq+%22vector+table%22&hl=en&lr=&ie=UTF-8&oe=UTF-8...>



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Searched the web for file type "vector table".
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DAG Project: DAG Boot and Monitor

... is a jump around the interrupt vector table to location ... ARM IRQ exception (vector=0x18),
 ... not applicable, not ... ARM FIQ exception (vector=0x1c), not applicable, not ...
 ... not applicable, not ...
 dag.cs.waikato.ac.nz/dag/dag-monitor.html - 11k - Cached - Similar pages

Samsung S3C2410 ARM920T bootcode written by PALADIN

... b HandlerRq ;handler for IRQ interrupt 0x18 ... HandlerFq ;handler for FIQ interrupt 0x1C ... bne LOOP ;=== Exception Vector Table Setup ...

[www.x-x.jp/embedded/dev_notes/2410init_030701.htm](#) - 16k - [Cached](#) - [Similar pages](#)

```
... <endpg> </ifq> <isamp;quot> <endif> <pre /> jump vector table as m... b software_interrupt
b abort; getetch b abort_data b nol_used b irq b irq_reset; //ispn ...
openarm.linuxforum.net/doc/BooLoader/start.s - 2K - Cached - Similar pages
```

HPAB
[PDF]

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... After it comes back from STARTUPCODE the vector table is copied from rom to ram ... table base address (physical) and whether angel uses the FIQ or IRQ interrupt ...

yyv.tl.kh:seidu/gu/fingerinfo/keys/finger/Mobile_VT98angel2bodge-rx2.pdf - Similar pages

RTMS ARM Applications Supplement

File format: P-Ur-Adoone Acrobat - View as HTML
... bit 6 (0 is least significant bit) disables the FIQ ... bit 7 (0 is least significant bit) disables the IRQ ... 0 (bytes 0-3) of the exception vector table (EVT) ...
www.microcross.com/rtems-am-supplement.pdf - Similar pages

[P5] RTMS ARM Applications Supplement

File format: Adobe Postscript - View as text
... **FIQ** Setting bit 6 (0) is least significant bit disables the **FIQ** ... Setting bit 7 (0
is least significant bit) disables the **IRQ** ... of the exception vector table **EVT** ..
www.items.com/online/docs/doc-current/share/items/ps/lamm.ps - Similar pages

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... 15 5.2 The Vector Table 15
5.3 Memory Setup ...
www.arm.com/pdfs/DAI0107A_Sw_dev_ADSv1_2.pdf - Similar pages

[PDF] AT91 ARM Thumb MCUS

File P-format: P-D/Ardebe ACK Miki AS H ML
 ... NWDOFQ TMS TDO TDI TCK MCKI PZSMCK P1ZF1Q P9IR00 P10 ... S eight levels of priority
 a hardware vector table 0x00000000 0x00000018 Core IRQ Vector Id# ...
 www.iopbent.com/wilibrary/product%20line/Arme/Products%20Presentation%20Data/
 Arme/ARM_Jan2001_James.pdf - Similar pages

지도표수: 집 회식 표수
...참조하면 된다.

<http://www.google.com/search?q=fiq+irq+%a22vector+table%a22&hl=en&lr=&ie=UTF-8&oe=UTF-8...>

User FIQ Super Abort IRQ Undefined. ... Vector Table ; ...
sun.uos.ac.kr/info/microc.htm - 101k - Cached - Similar pages

PDF Application Note 25

File Format: PDF/Adobe Acrobat
... IRQ Occurs when the CPU's external interrupt request ... FIQ Occurs when the CPU's external fast interrupt ... DAI 0025E 3.1.1 The vector table Exception handling ...
ftp.cis.indu.edu.tw/data/networker/docs/arm/Arp25E.pdf - Similar pages

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Searched the web for fig11q "vector table".
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DAG Project: DAG Boot and Monitor

... is a jump around the interrupt vector table to location ... ARM IRQ exception (vector=0x18), not applicable, not ... ARM FIQ exception (vector=0x1c), not applicable, not ...
dag.cs.waikato.ac.nz/dag/dag-monitor.html - 11k - Cached - Similar pages

Samsung S3C2410 ARM920T bootcode written by PALADIN

... b HandlerIRQ_handler for IRQ interrupt 0x18 ... HandlerFIQ_handler for FIQ interrupt 0x1C ... bne LOOP ===== Exception Vector Table Setup ...

www.x-rtx.jp/embed/dev_notes/2410/init_030701.htm - 16k - [Cached](#) - [Similar pages](#)

```
<html><head></head><body><pre>&lt;html&gt; &lt;head&gt; &lt;/pre>
```

```
... &amp;quot;config.h&amp;quot;:endif: text / Jump vector table as in ... b software_interrupt
b about_prefetch b about_data b not_used b irq b irq_reset: //sign ...
openann.linuxforum.net/doc/Boo loader/start.s - 26k - Cached - Similar pages
```

HPAB
[Prof]

File Format: PDF/Acrobat - View as HTML
... After it comes back from STAIRCODE the vector table is copied from rom to ram ... table base address (physical) and whether angel uses the FIQ or IRQ interrupt ...
v1.kh:seidu/gnu/finemto/teleys.finger.Mobile.VT98/angel2badge-ev2.pdf Similar pages

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... bit 6 (0 is least significant bit) disables the FIQ. ... bit 7 (0 is least significant bit) disables the IRQ. ... 0 (bytes 0-3) of the exception vector table (EVT). ...
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(PS) RTEMS ARM Applications Supplement

File Format: Adobe PostScript - View as Text

... FIQ. Setting bit 7 (0 ... least significant bit) disables the FIQ. ... Setting bit 7 (0 ... least significant bit) disables the IRQ. ... of the exception vector table (EVT ...

www.arm.com/infocdocs/ddc-current/sharw/temsp/psarm.ps - Similar pages

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... 15
5.2 The Vector Table 15
5.3 Memory Setup ...
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File Format: PDF/Acrobat - View as HTML
 ... NNDFOF TMS TDO TDI TDC MGCKI PZSMGCK P-12/FIQ P9/IRQ P-10 ... s Eight levels of priority
 Hardware vector table 0x00000000 0x00000018 Core IRQ Vector tdr ...
 www.ti.com/lit/zip/product/20line/ARMelf/Product%20Presentation%20Data/
 utmel_ARM_Jan2001_jlames.pdf - Similar Pages

지도교수: 김희식 교수님
...참조하면 된다. —

User FIQ Super Abort IRQ Undefined. ... Vector Table : ...
sun.uos.ac.kr/info/microc.htm - 101k - Cached - Similar pages

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 ... IRQ Occurs when the CPU's external interrupt request ... FIQ Occurs when the CPU's external fast interrupt ... DAI 0025E 3.1.1 The vector table Exception handling ...
 ip.cis.nctu.edu.tw/data/networker/docs/amr/Apps25/E.pdf - Similar pages

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